



digital circuit experiment

By ZHENG JIANG QI HAI FENG

paperback. Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment. Pages Number: 252 Publisher: Nanjing University Press Pub. Date :2008-09. The materials can be divided into five parts: standards for small and medium-scale digital integrated circuits circuit . CPLD. microcontroller. digital circuit experiments and experiments in the use of special chips more information. Small and medium-scale integrated circuits in standard digital circuitry. the editor used to design the internal circuits of the EWB simulation of the 74LS00 to enhance students characteristics of integrated circuit chips in digital knowledge. HC4046-based phase-locked loop circuit design. requiring students to design second-order digital data according to the chip PLL. In the CPL. D application experiment. ask students to learn Verilog from simple to complex language. In the experimental circuit board. circuit design with no anti-shake and anti-shake button. the button circuit. so that students understand the practical problems that may occur in the circuit. In the single-chip experiment. emphasizing the relationship microcontroller circuits and assembly language. The three parts will do for the curricular content. Digital circuit experiment generally separate class for 4 hours per week. a total of 68 semester hours. total 2...



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